

**UTILITY
PATENT APPLICATION
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 245824US2

First Inventor or Application Identifier Kunihiko YAHAGI

Title MEMORY CONTROLLER, SEMICONDUCTOR INTEGRATED CIRCUIT, AND METHOD FOR CONTROLLING A MEMORY

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents

1. Fee Transmittal Form (e.g. PTO/SB/17)
(Submit an original and a duplicate for fee processing)

2. Specification

Total Sheets

26

3. Drawing(s) (35 U.S.C. 113) Total Sheets

7

4. Oath or Declaration Total Pages

[]

a. Newly executed (original or copy)
b. Copy from a prior application (37 C.F.R. §1.63(d))
(for continuation/divisional with box 17 completed)

i. DELETION OF INVENTOR(S)

Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).

5. CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)

6. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)

a. Computer Readable Form (CRF)

b. Specification or Sequence Listing on :

i. CD-ROM or CD-R (2 copies); or
ii. Paper

c. Statements verifying identity of above copies

Commissioner for Patents
ADDRESS TO: Mail Stop Patent Application
Alexandria, Virginia 22313

ACCOMPANYING APPLICATION PARTS

7. Assignment Papers (cover sheet & document(s))
8. Application Data Sheet. See 37 CFR 1.76
9. 37 C.F.R. §3.73(b) Statement
(when there is an assignee) Power of Attorney
10. English Translation Document (if applicable)
11. Information Disclosure Statement (IDS)/PTO-1449 Copies of IDS Citations (1)
12. Preliminary Amendment
13. White Advance Serial No. Postcard
14. Certified Copy of Priority Document(s) (1)
(if foreign priority is claimed)
15. Applicant claims small entity status.
See 37 CFR 1.27
16. Other: Request for Priority, Statement of Relevancy

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:

Continuation Divisional Continuation-in-part (CIP) of prior application no.:

Prior application information: Examiner:

Group Art Unit:

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. CORRESPONDENCE ADDRESS

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13281
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Docket No. 245824US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Kunihiko YAHAGI

SERIAL NO: New Application

FILING DATE: Herewith

FOR: MEMORY CONTROLLER, SEMICONDUCTOR INTEGRATED CIRCUIT, AND METHOD FOR CONTROLLING A MEMORY

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	20 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$86 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$290 =	\$0.00
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The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLOON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.Date: 11/21/03

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